



Docket No. TI-35286

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re **PATENT** application of:

Applicant: Bryan D. Sheffield et al.

Application No.: 10/663,144

For: METHOD OF OPERATING A MEMORY AT HIGH SPEED
USING A CYCLE READY STATUS OUTPUT SIGNAL

Filing Date: September 16, 2003

Examiner: (presently unknown)

Art Unit: (presently unknown)

PRELIMINARY AMENDMENT

Mail Stop Non-Fee Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

Favorable reconsideration of the above-identified application is respectfully
requested in view of the following remarks.